

**WEST**

## Freeform Search

**Database:**

US Patents Full-Text Database  
US Pre-Grant Publication Full-Text Database  
JPO Abstracts Database  
EPO Abstracts Database  
Derwent World Patents Index  
IBM Technical Disclosure Bulletins

**Term:**

L7 and boundary with scan\$4 and (global or system  
or core or main)near2 (clock\$3 or timing or sync  
or synchron\$7 or sinchron\$7) and intern\$4 near(bus  
or line)with (test\$3 or debug\$4) and register with

**Display:**

20

**Documents in Display Format:**

-

**Starting with Number**

1

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Side by Side

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Image

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Main Menu

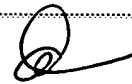
Show S Numbers

Edit S Numbers

Preferences

Cases

### Search History

**DATE:** Friday, April 18, 2003[Printable Copy](#)[Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L9</u>	('5812830'  '5951696'  '5701308')[ABPN1,NRPN,PN,TBAN,WKU] L7 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and intern\$4 near(bus or line)with (test\$3 or debug\$4) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3)	6	<u>L9</u>
<u>L8</u>	L6 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and intern\$4 near(bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3) and (test\$3 or debug\$4)	3	<u>L8</u>
<u>L7</u>	L5 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3) and (test\$3 or debug\$4)	38	<u>L7</u>
<u>L6</u>	L4 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3) and (test\$3 or debug\$4)	87	<u>L6</u>
<u>L5</u>	L3 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3)	87	<u>L5</u>
<u>L4</u>	L2 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3)	284	<u>L4</u>
<u>L3</u>	L1 and boundary with scan\$4	509	<u>L3</u>
<u>L2</u>	ieec\$1149\$2	1895	<u>L2</u>
<u>L1</u>		134151	<u>L1</u>

END OF SEARCH HISTORY

# WEST Search History

DATE: Wednesday, October 15, 2003

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
L9	('5812830  '5951696  '5701308')[ABPN1,NRPN,PN,TBAN,WKU]	6	L9
L8	((('5812830  '5951696  '5701308')[ABPN1,NRPN,PN,TBAN,WKU]) AnD ((@pd > 20030418)!)	0	L8
L7	(L6 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3) and (test\$3 or debug\$4)) AnD ((@pd > 20030418)!)	10	L7
L6	(L5 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3)) AnD ((@pd > 20030418)!)	10	L6
L5	(L4 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3)) AnD ((@pd > 20030418)!)	33	L5
L4	(L3 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7)) AnD ((@pd > 20030418)!)	65	L4
L3	(L2 and boundary with scan\$4) AnD ((@pd > 20030418)!)	216	L3
L2	(ieee\$1149\$2) AnD ((@pd > 20030418)!)	11544	L2
L1	6181151.pn.	2	L1

END OF SEARCH HISTORY

## WEST Search History





DATE: Tuesday, January 27, 2004

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L19	('6665816')!.ABPN1,NRPN,PN,TBAN,WKU.	2
		<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L18	((('5812830'  '5951696'  '5701308') [ABPN1,NRPN,PN,TBAN,WKU])) and @pd > 20031015	0
<input type="checkbox"/>	L17	(L15 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and intern\$4 near(bus or line)with (test\$3 or debug\$4) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3)) and @pd > 20031015	0
<input type="checkbox"/>	L16	(L14 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and intern\$4 near(bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3) and (test\$3 or debug\$4)) and @pd > 20031015	3
<input type="checkbox"/>	L15	(L13 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3) and (test\$3 or debug\$4)) and @pd > 20031015	0
<input type="checkbox"/>	L14	(L12 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3) and (skew\$3 or de\$1skew\$3)) and @pd > 20031015	3
<input type="checkbox"/>	L13	(L11 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7) and (bus or line) and register with (instruct\$3 or command\$3)) and @pd > 20031015	0
<input type="checkbox"/>	L12	(L10 and boundary with scan\$4 and (global or system or core or main)near2 (clock\$3 or timing or sync or synchron\$7 or sinchron\$7)) and @pd > 20031015	40
<input type="checkbox"/>	L11	(L9 and boundary with scan\$4) and @pd > 20031015	0
<input type="checkbox"/>	L10	(ieee\$1149\$2) and @pd > 20031015	6460
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L9	('6014752')!.PN.	2
<input type="checkbox"/>	L8	14 and 'tap' with control\$4 with '1149' with (state or machine) and sync	0
<input type="checkbox"/>	L7	14 and 'tap' with control\$4 with '1149' with (state or machine) and skew\$3	0
<input type="checkbox"/>	L6	14 and 'tap' with control\$4 with '1149' with (state or machine) and de\$2skew\$3	0
<input type="checkbox"/>	L5	14 and 'tap' with control\$4 with '1149' with (state or machine) same de\$2skew\$3	0
<input type="checkbox"/>	L4	11 and 'tap' with control\$4 with '1149' with (state or machine)	17
<input type="checkbox"/>	L3	11 and 'tap' with control\$4 with '1149' and finite near3 (state or machine)	0



<input type="checkbox"/>	L2	l1 and 'tap' with control\$4 with '1149' same finite near3 (state or machine)	0
<input type="checkbox"/>	L1	'tap' with control\$4 with '1149'	20

END OF SEARCH HISTORY

**WEST**

Generate Collection

Print

L124: Entry 3 of 10

File: USPT

Apr 16, 2002

DOCUMENT-IDENTIFIER: US 6373900 B1

TITLE: Multi-pair transceiver decoder system with low computation slicer

US PATENT NO. (1):6373900Detailed Description Text (27):

In the illustrated embodiment of the gigabit transceiver of FIG. 2, the trellis decoder 38 suitably includes an 8-state Viterbi decoder for symbol decoding, and incorporates circuitry which implements a decision-feedback sequence estimation approach in order to compensate the ISI components perturbing the signal which represents transmitted symbols. The 4D output 40 of the trellis decoder 38 is provided to the receive section 204R of the PCS block. The receive section 204R of PCS block de-scrambles and further decodes the symbol stream and then passes the decoded packets and idle stream to the receive section of the GMII block 202 for transfer to the MAC module.